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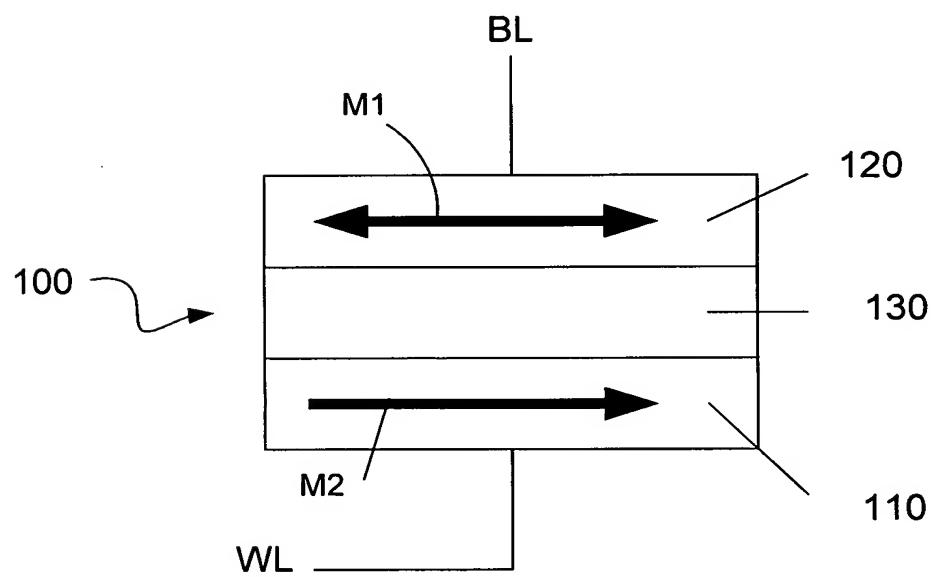


FIGURE 1 (PRIOR ART)

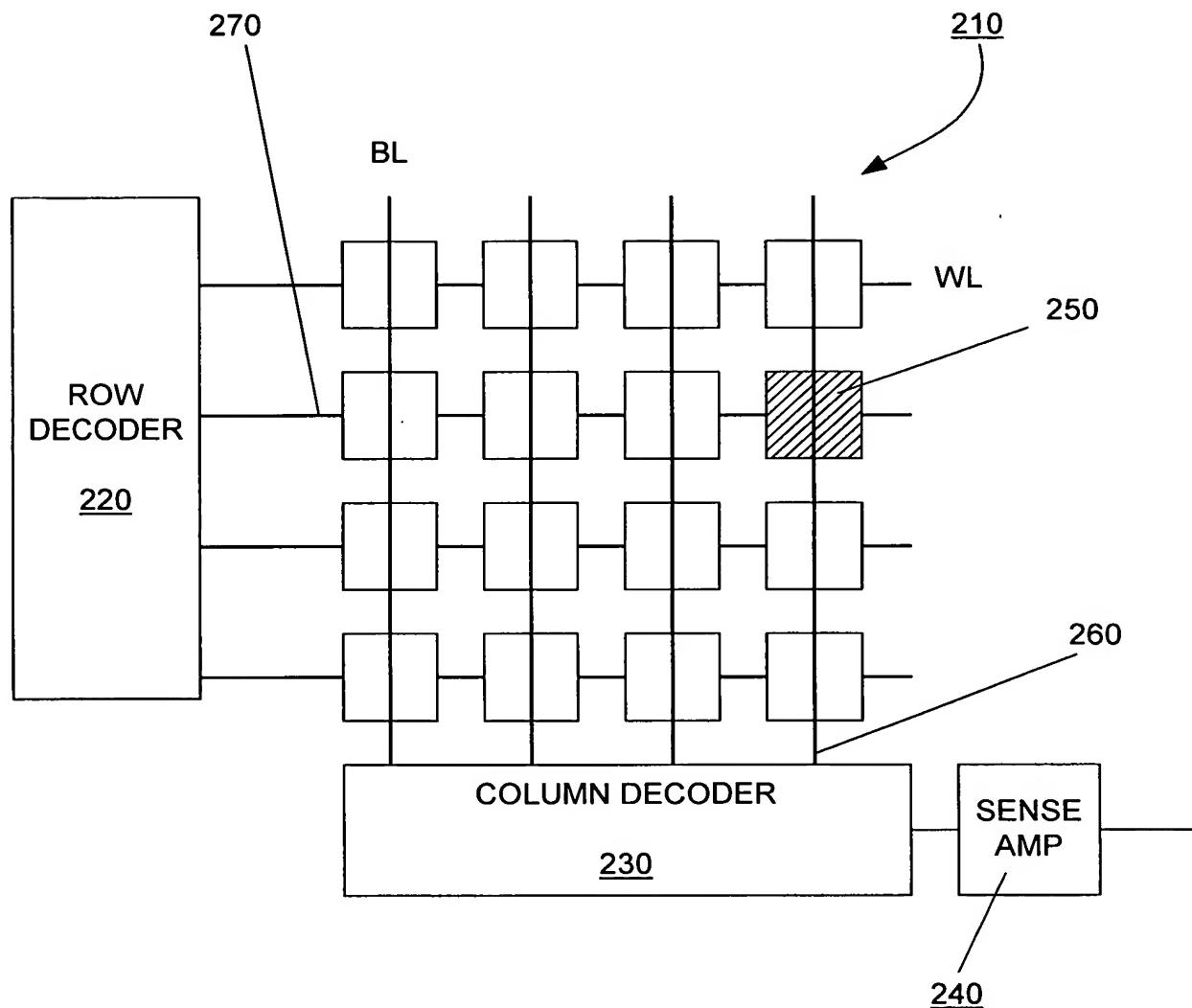


FIGURE 2 (PRIOR ART)

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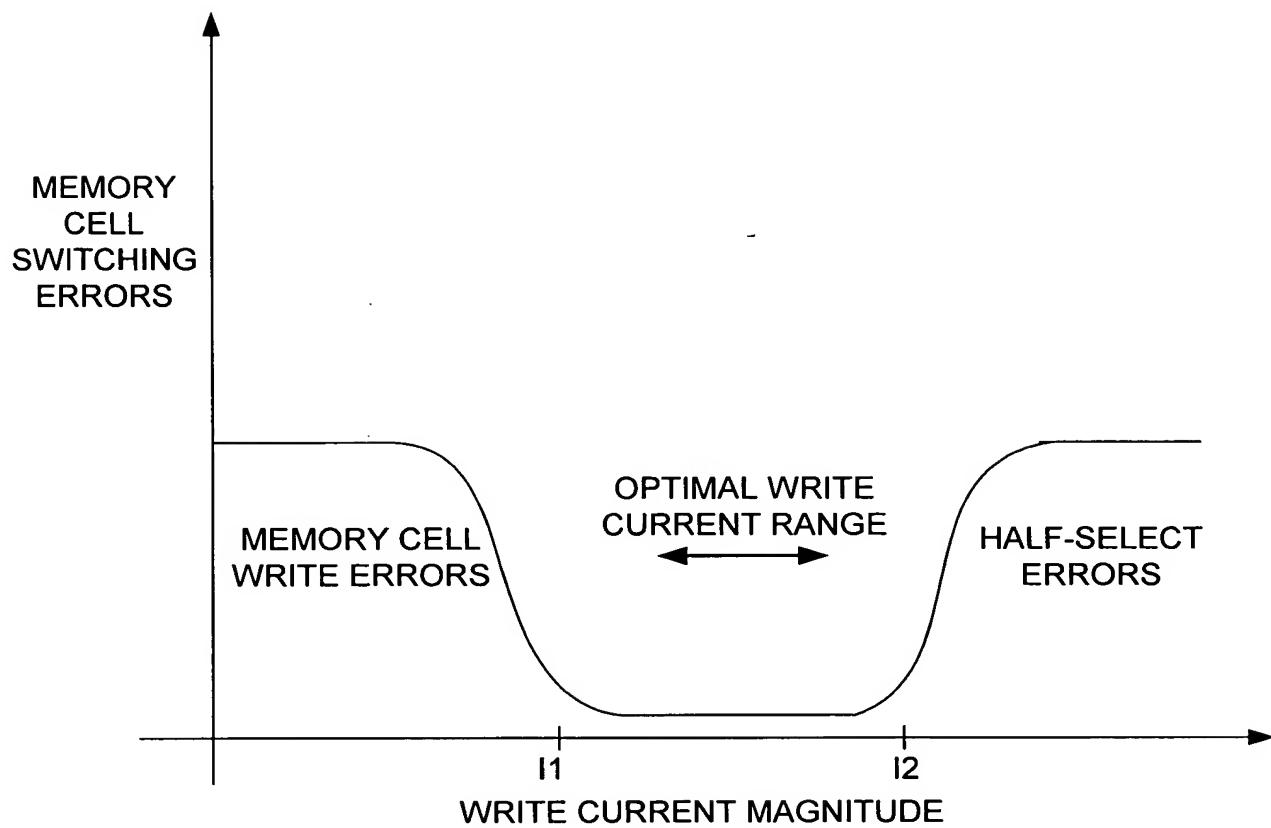


FIGURE 3

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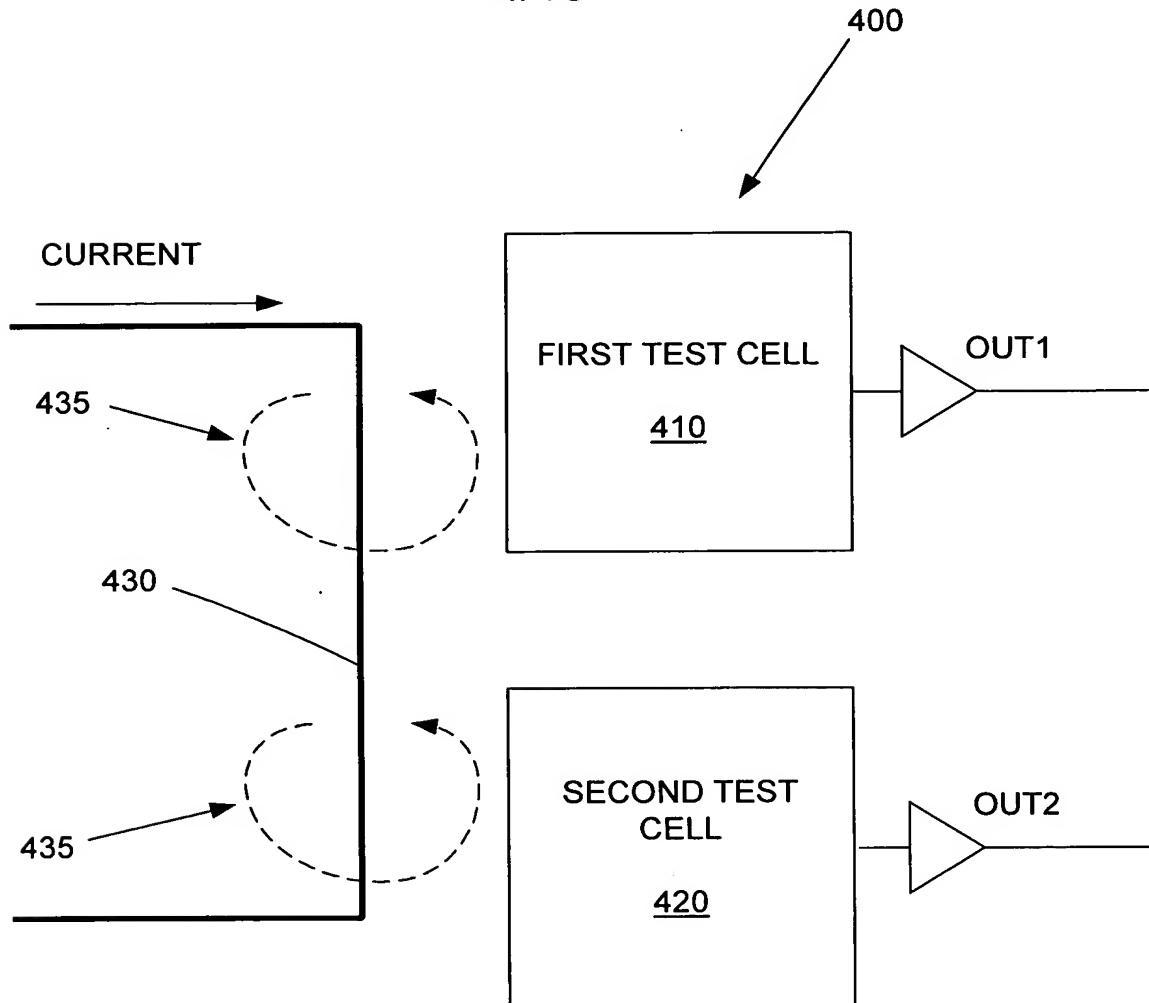


FIGURE 4

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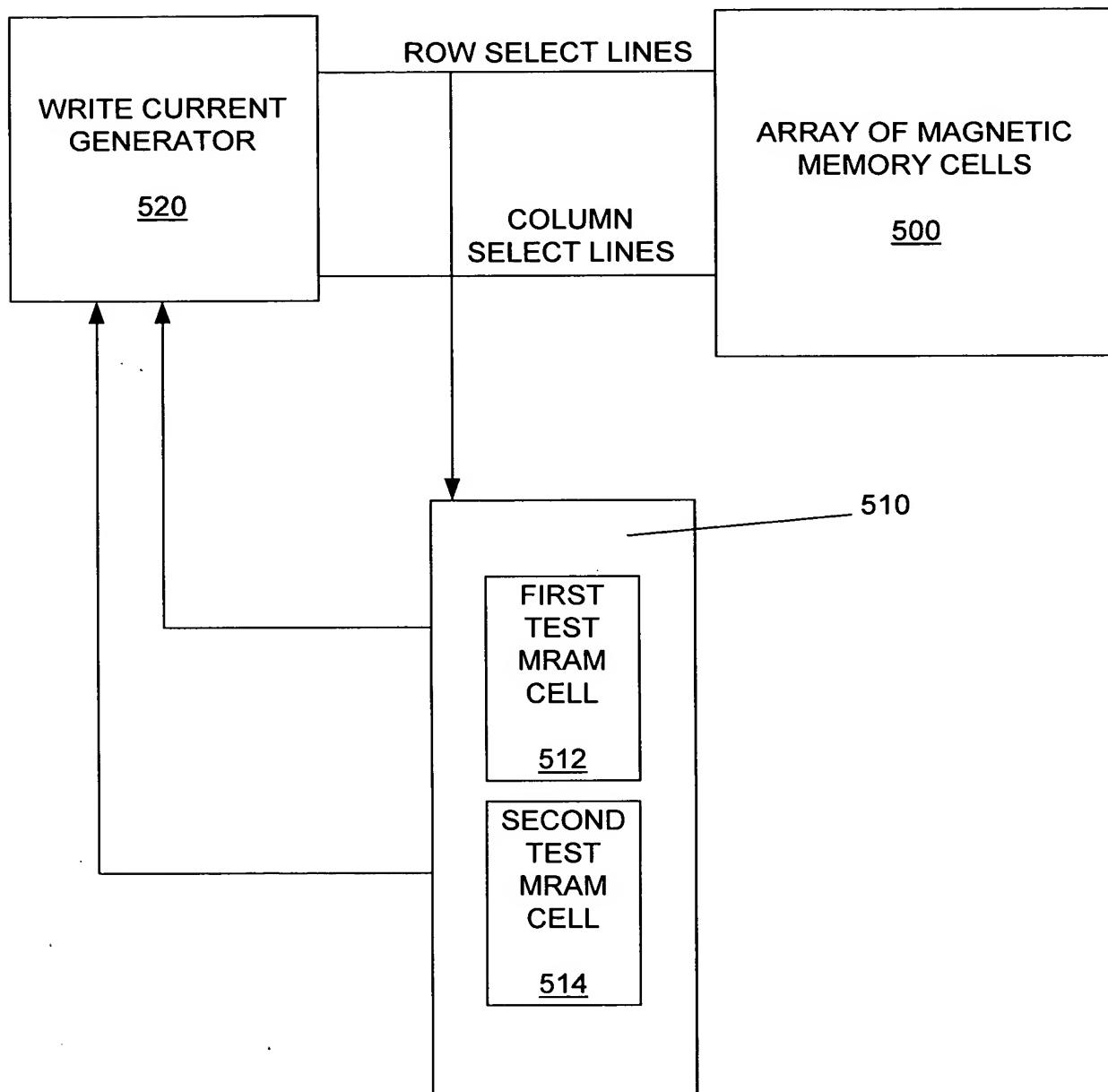


FIGURE 5

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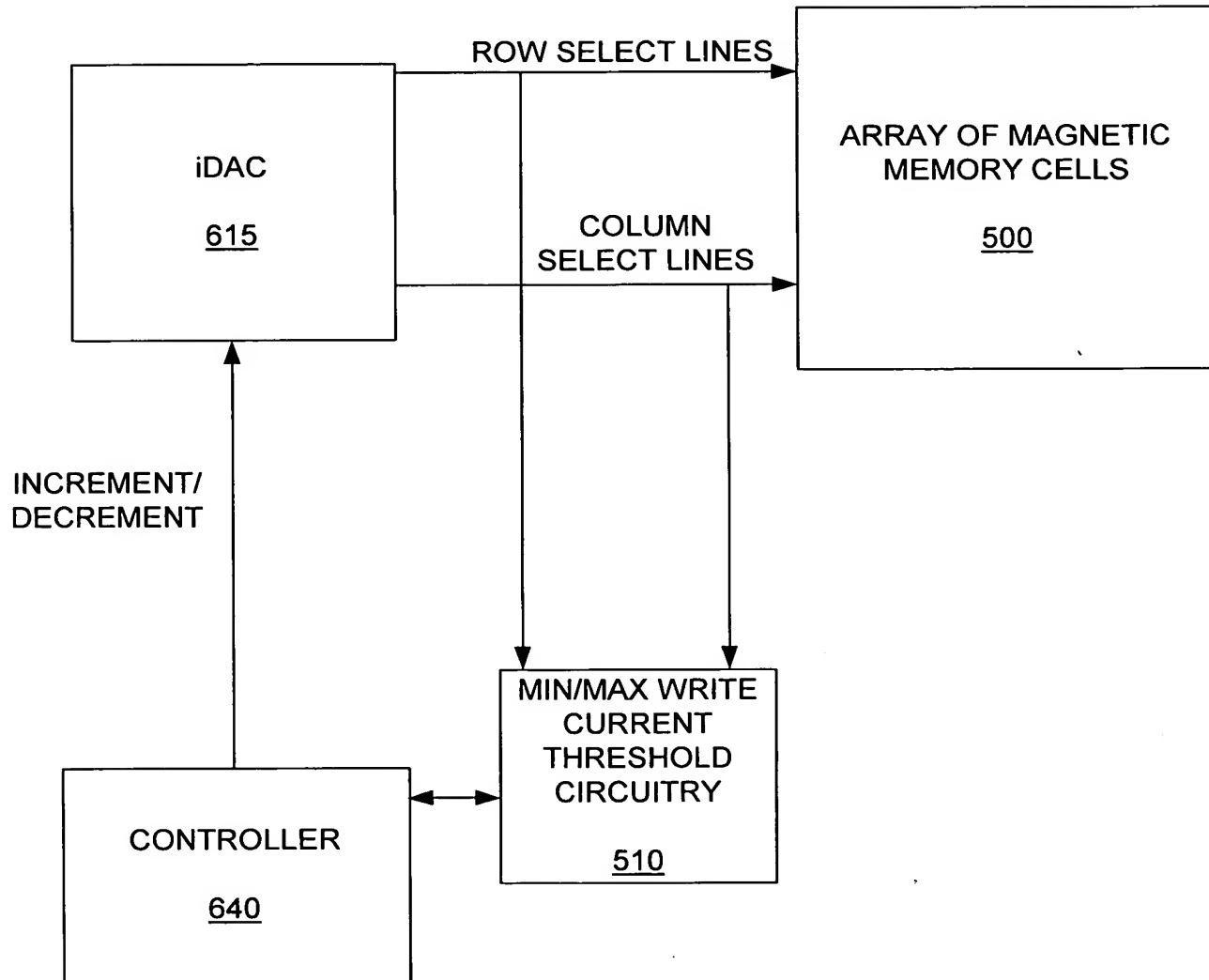


FIGURE 6

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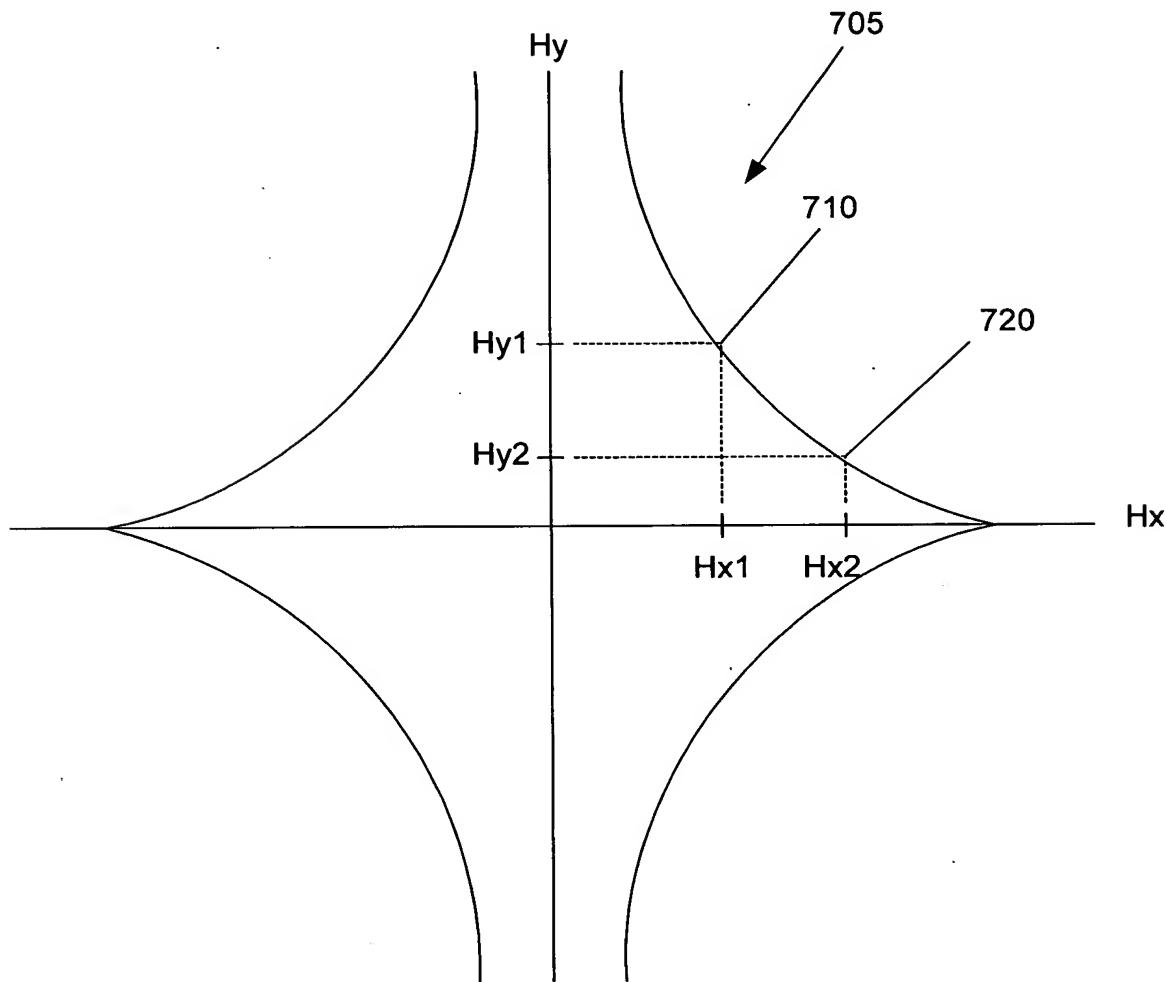


FIGURE 7

HEWLETT-PACKARD COMPANY, ATTORNEY DOCKET NUMBER 200310068-1, INVENTORS: PERNER,  
BHATTACHARYYA, TITLE: A CURRENT THRESHOLD DETECTOR

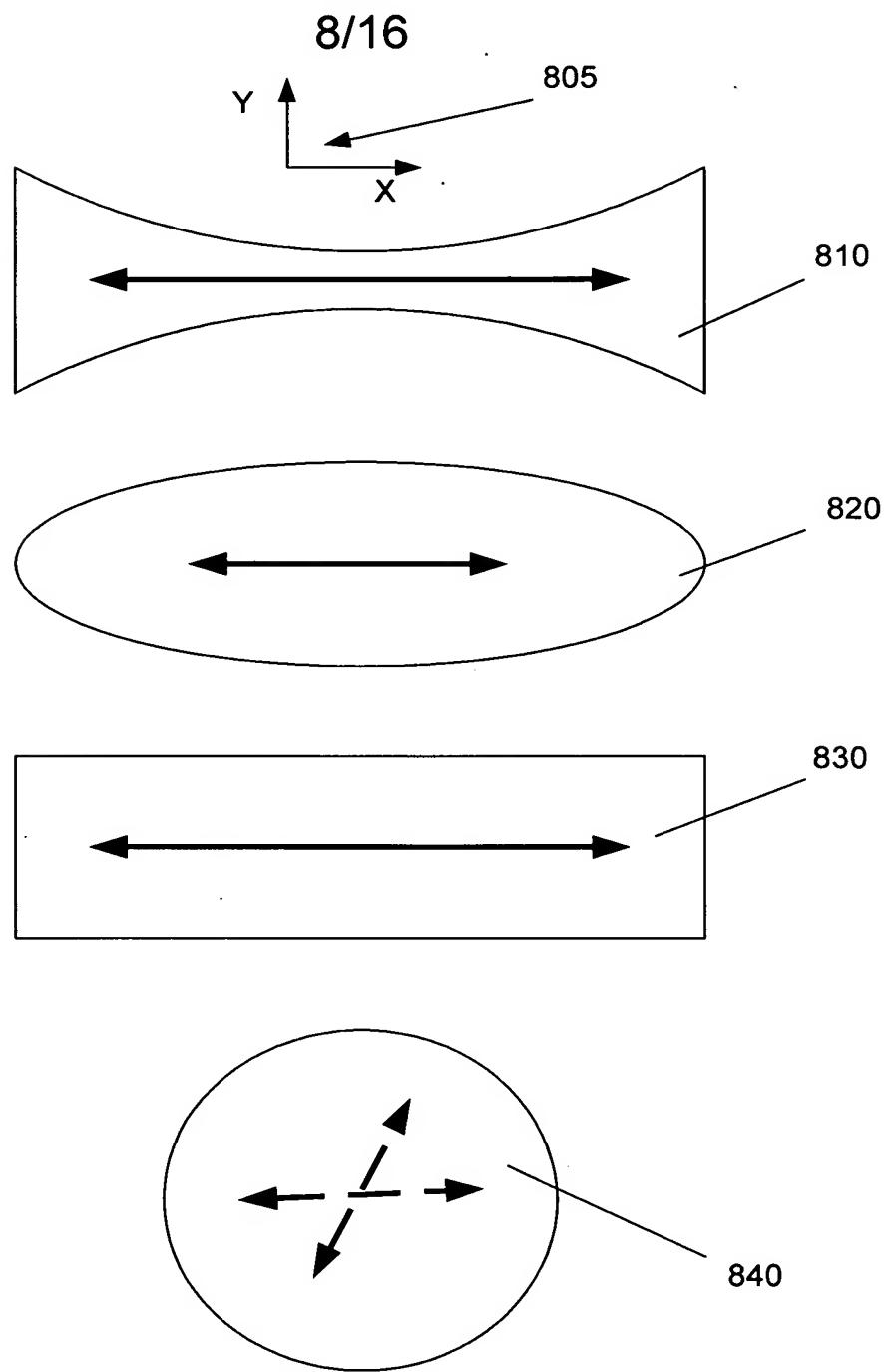


FIGURE 8

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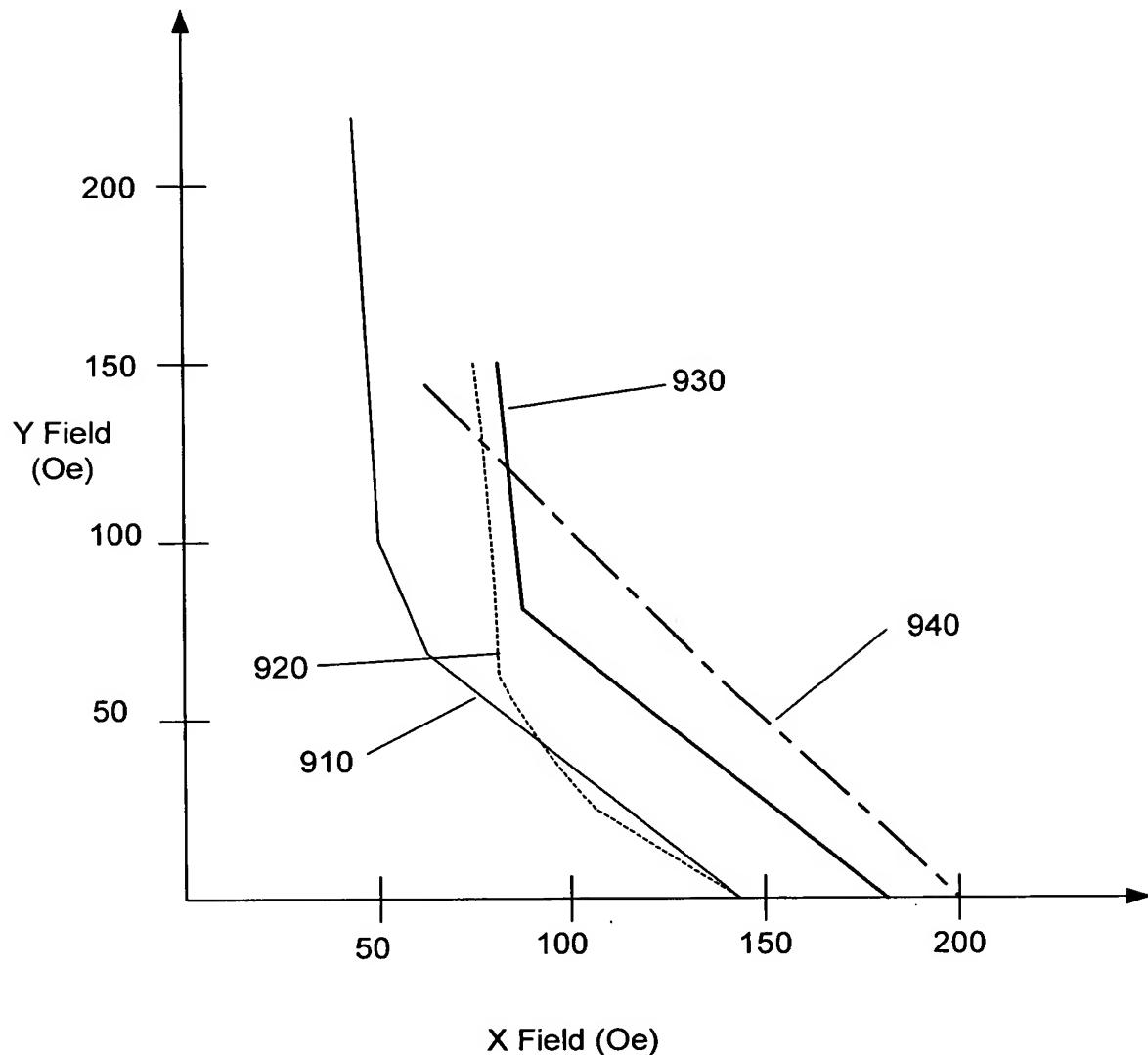


FIGURE 9

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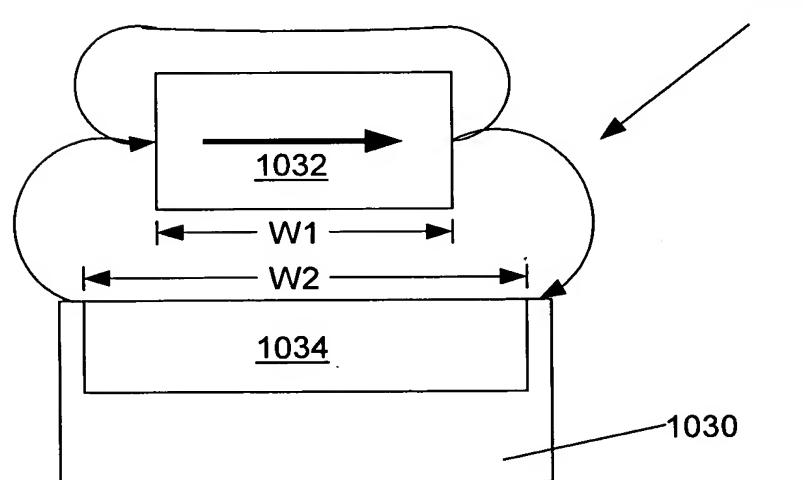
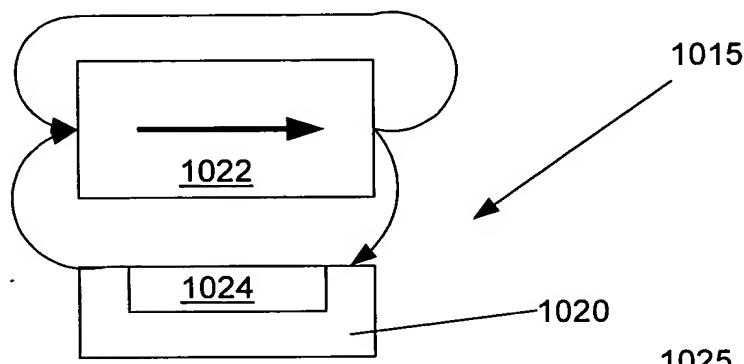
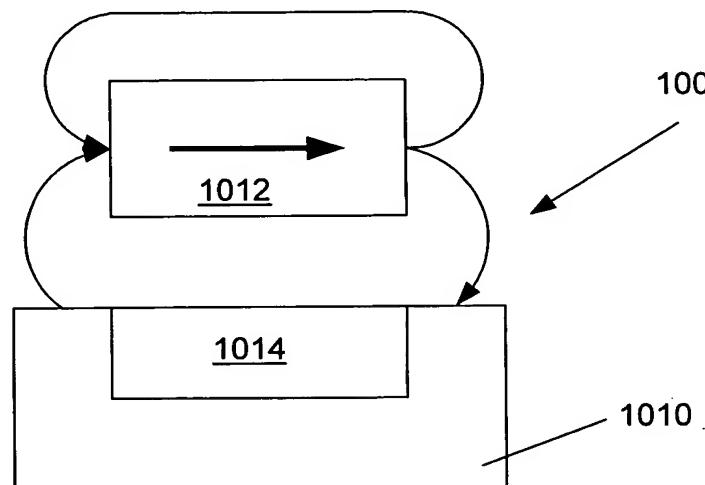
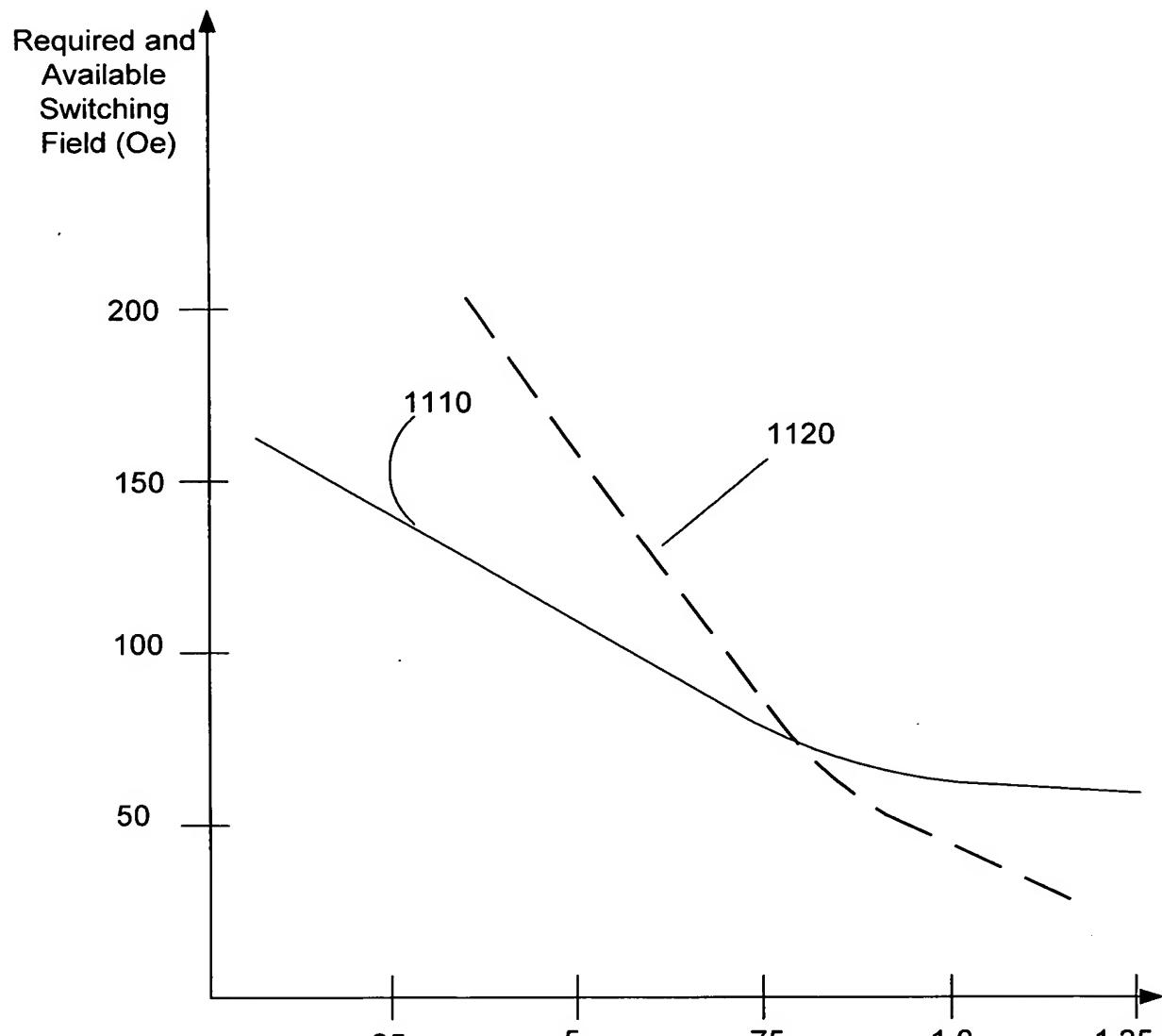


FIGURE 10

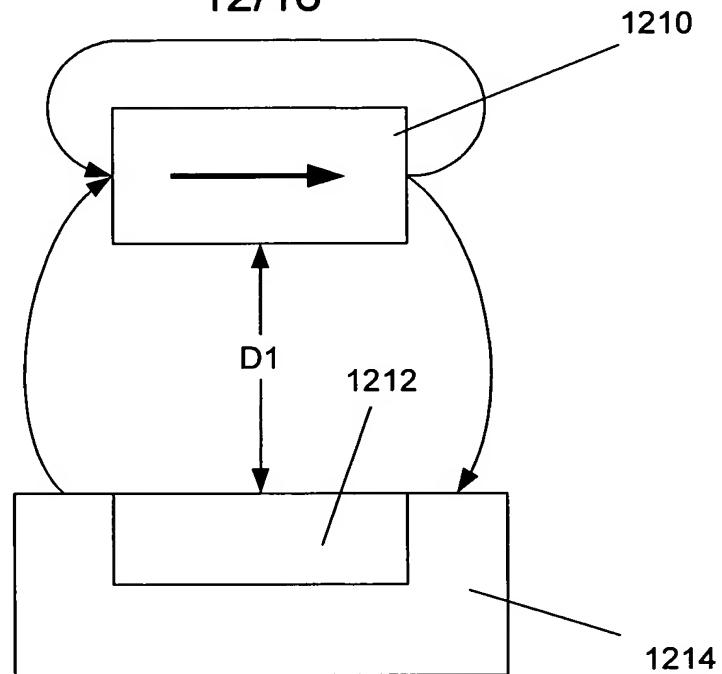


W2/W1 Ratio of Conductive Write Line and Memory Cell

FIGURE 11

HEWLETT-PACKARD COMPANY, ATTORNEY DOCKET NUMBER 200310068-1, INVENTORS: PERNER,  
BHATTACHARYYA, TITLE: A CURRENT THRESHOLD DETECTOR

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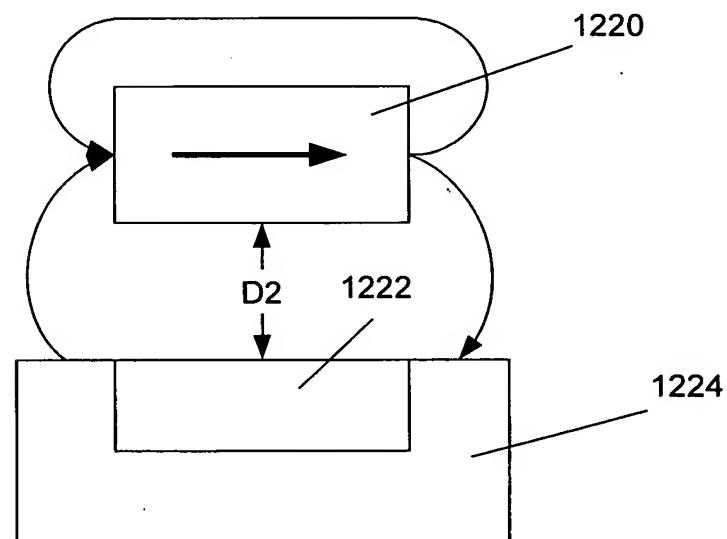


FIGURE 12

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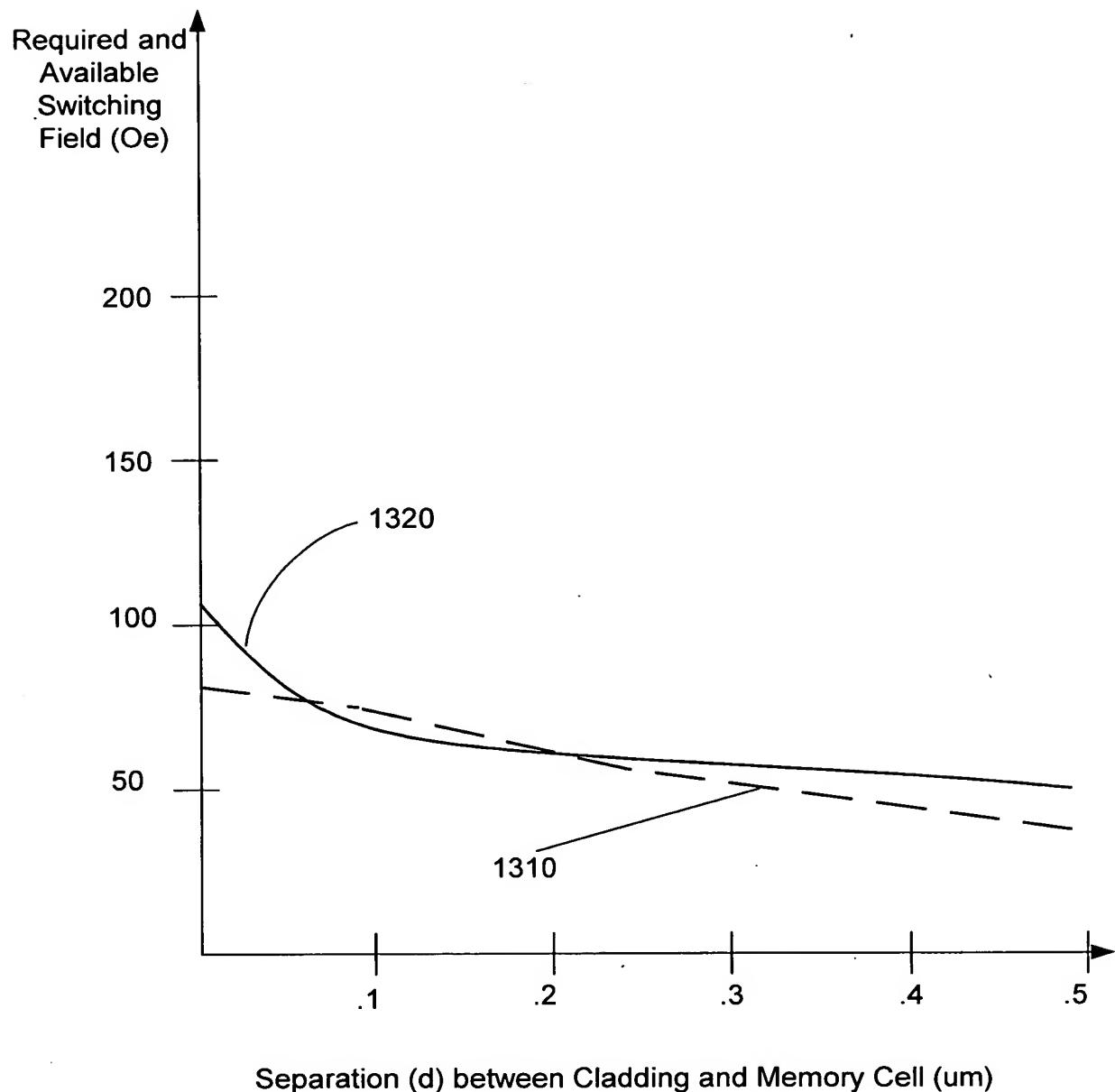


FIGURE 13

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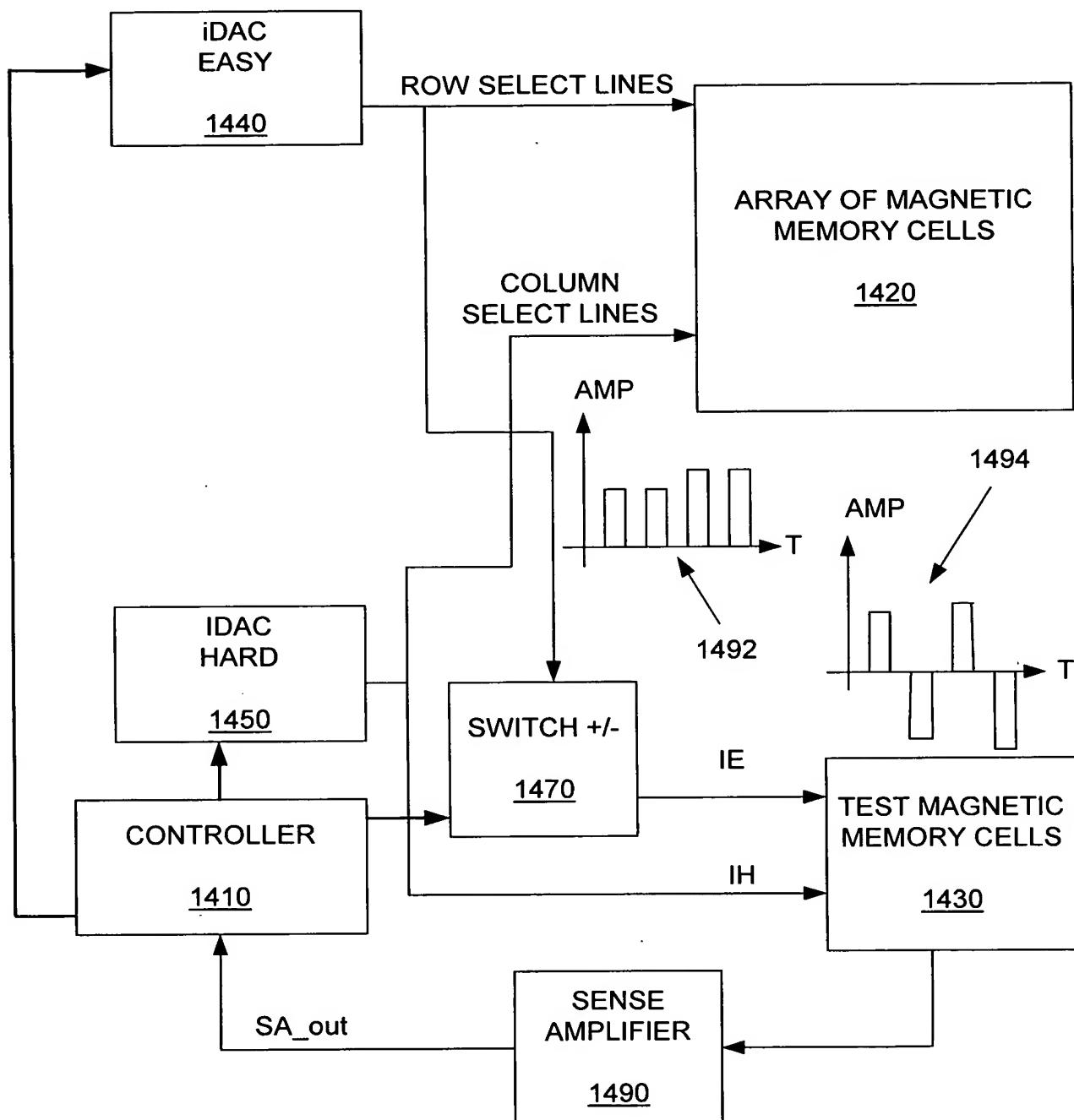


FIGURE 14

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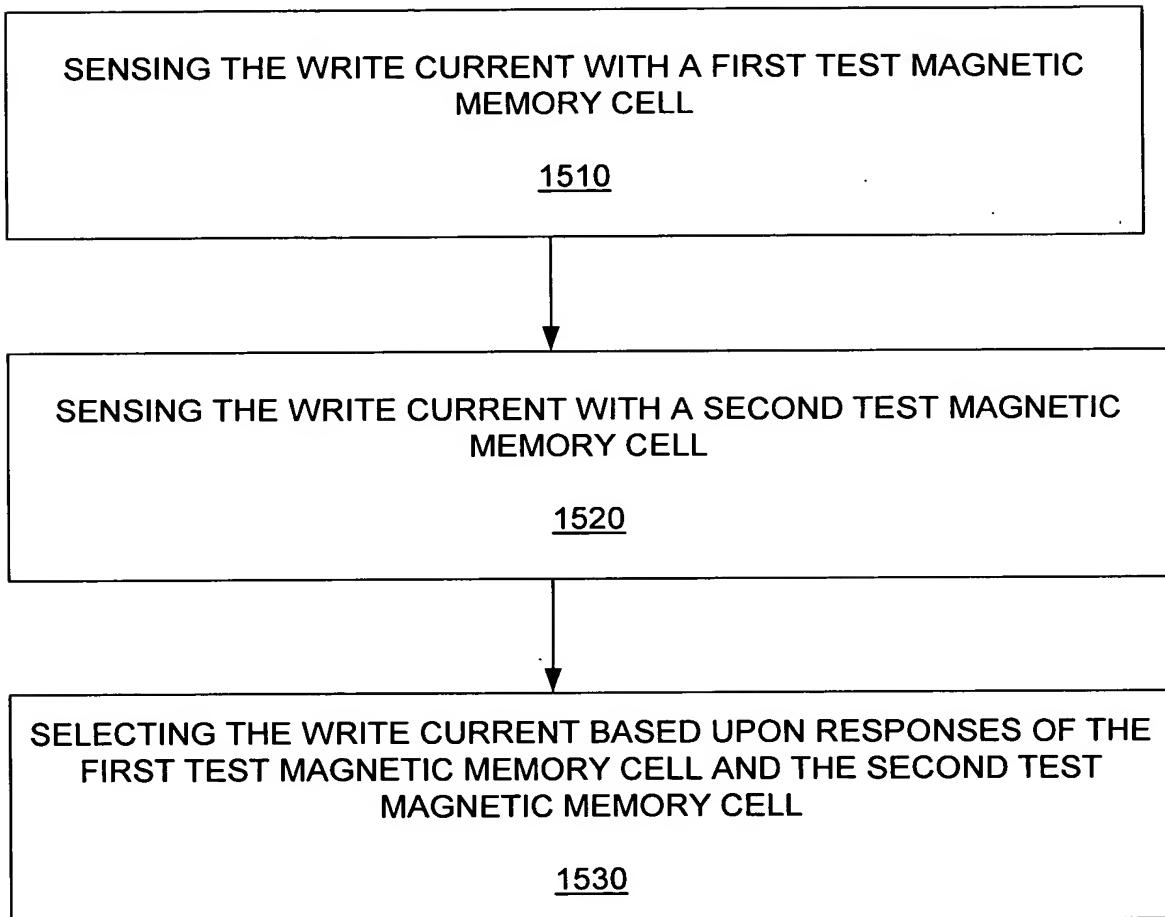


FIGURE 15

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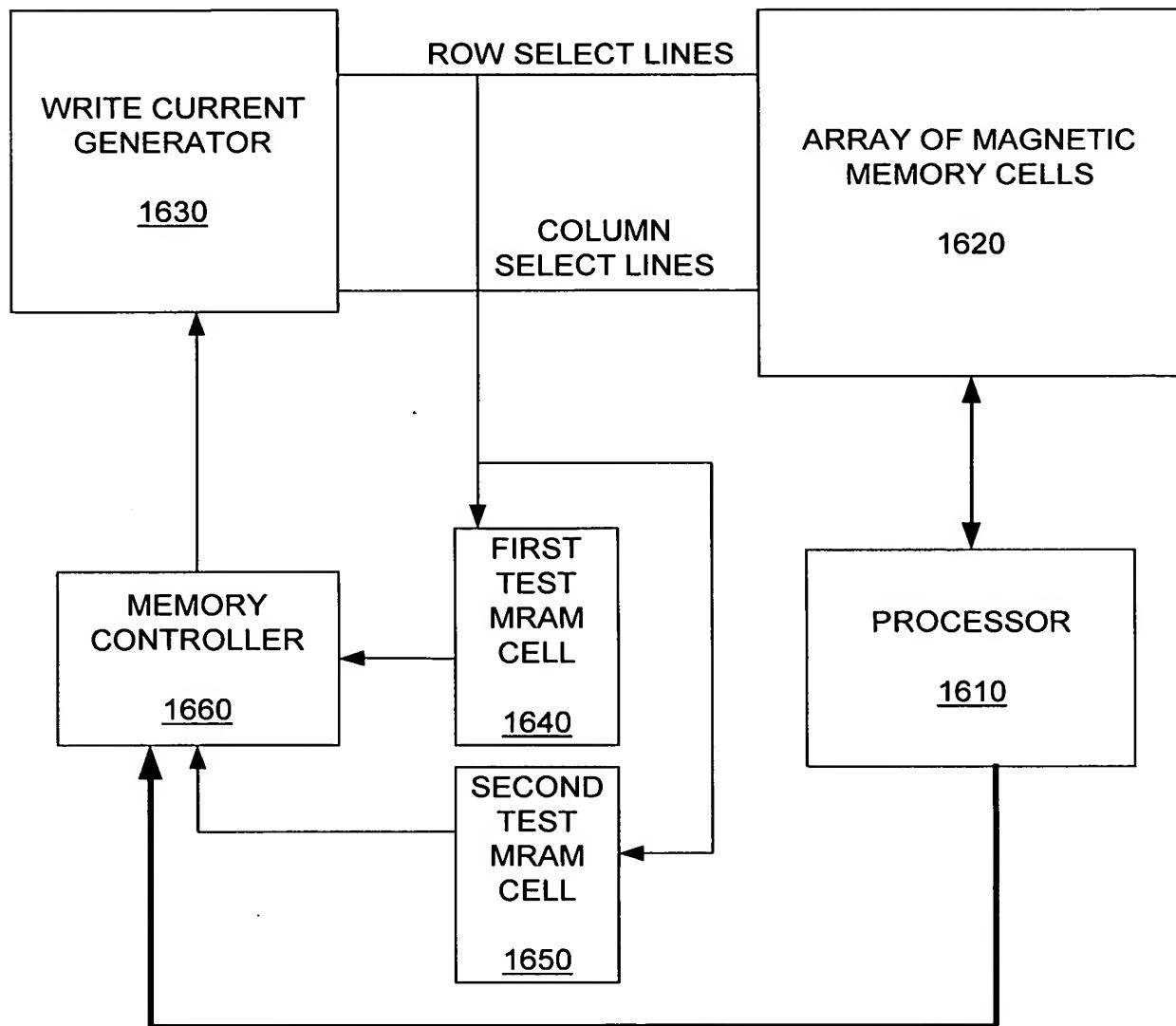


FIGURE 16